

Europäisches Patentamt

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(11) EP 0 901 181 A2

(12) EUROPEAN PATENT APPLICATION

(43) Date of publication:
10.03.1999 Bulletin 1999/10

(51) Int. Cl. 6: H01P 5/08

(21) Application number: 98116632.5

(22) Date of filing: 02.09.1998

(84) Designated Contracting States:
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE
Designated Extension States:
AL LT LV MK RO SI

(30) Priority: 04.09.1997 US 923314

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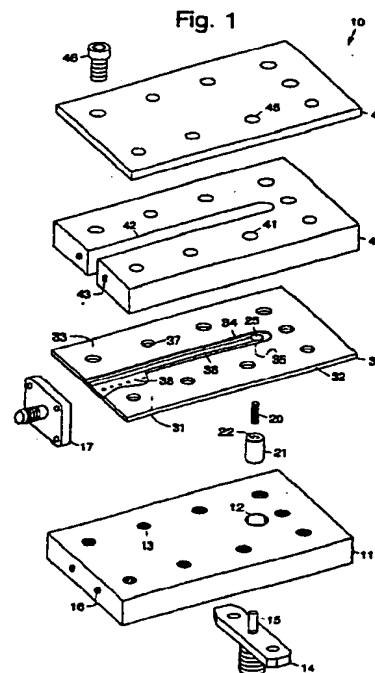
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(54) Microstrip to coax vertical launcher using conductive, compressible and solderless interconnects

(57) Coax to microstrip orthogonal launchers (10) that use a compressible fuzz button center conductor as a solderless interconnect. The launcher comprises a coaxial connector (14) having a center conductor (15) that contacts a compressible fuzz button interconnect (20). In certain embodiments, the fuzz button interconnect directly contacts one end of a microstrip line (36). In another embodiment, the microstrip line is formed on a curved microstrip circuit board (30), and the fuzz button interconnect contacts a pin (47) that has a thin metal tab (48) that is adhesively secured to the one end of the microstrip line. In all embodiments, a second coaxial line (17) has a center conductor that contacts the opposite end of the microstrip conductor line. The present invention eliminates need for precise soldering by using the fuzz button interconnect to create a solderless compression contact between the coaxial connector and the microstrip line. The present invention provides a simple way to vertically launch an RF signal onto microstrip transmission line from a coaxial cable and operates at frequencies up to 18 GHz.



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Description

BACKGROUND

[0001] The present invention relates generally to coax to microstrip orthogonal launchers, and more particularly, to coax to microstrip orthogonal launchers that use a compressible fuzz button center conductor as a solderless interconnection.

[0002] Current active array microstrip corporate feeds require precise soldering of wires onto a microstrip line through a machined hole or trough. For large arrays, the large number of vertical interconnects requiring this precise soldering in the feed requires a large amount of hands-on physical labor. Therefore, it would be an advance in the art to eliminate the requirement for precise soldering and thus lessen the amount of physical labor required to manufacture the array corporate feed.

[0003] Also a current state of the art vertical coax to microstrip launcher used by the assignee of the present invention operates up to a frequency of about 12 GHz. It would be an advance in the art to have a vertical coax to microstrip launcher that operates at a higher frequency.

[0004] Accordingly, it is an objective of the present invention to provide for coax to microstrip orthogonal launchers that use a compressible fuzz button center conductor as a solderless interconnection. It is a further objective of the present invention to provide for coax to microstrip orthogonal launchers that operate at a frequency substantially higher than conventional orthogonal launchers.

SUMMARY OF THE INVENTION

[0005] To accomplish the above and other objective the present invention provides for improved coax to microstrip orthogonal launchers that comprise a compressible fuzz button center conductor as a solderless interconnection. In general, the orthogonal coax to microstrip launcher comprises a coaxial connector having a center conductor that contacts a compressible fuzz button interconnect. In certain embodiments, the fuzz button interconnect contacts one end of a microstrip line. In another embodiment, the microstrip line is formed on a curved microstrip circuit board, and the fuzz button interconnect contacts a pin that has a thin metal tab that is adhesively secured to the one end of the microstrip line. In all embodiments, a second coaxial connector has a center conductor connected to the other end of the microstrip conductor line.

[0006] The necessity for precise soldering required by conventional coax to microstrip orthogonal launchers is greatly simplified if not eliminated by using the fuzz button interconnect to create a solderless compression contact between the center pin of the coaxial connector and the microstrip line. The present invention provides a simple way to vertically launch an RF signal onto microstrip transmission line from a coaxial cable. The present

invention operates at a frequency of up to 18 GHz, which is a wider frequency band than has been achieved in prior art devices. The use of compressible fuzz button interconnects eliminates the need for hard solder connectors required in previous hard wired designs.

[0007] The present invention was specifically designed for use on an active array antenna currently under development by the assignee of the present invention to interconnect transmit/receive modules to a first level microstrip feed within a subarray. The present invention may also be used to realize stack microstrip microwave integrated circuit modules for advanced receivers for use in radar and satellite applications, and low cost assemblies for commercial wireless communication equipment.

BRIEF DESCRIPTION OF THE DRAWINGS

[0008] The various features and advantages of the present invention may be more readily understood with reference to the following detailed description taken in conjunction with the accompanying drawings, wherein like reference numerals represent like structural elements, and in which:

Fig. 1 is an exploded isometric view of a first embodiment of an orthogonal coax to microstrip launcher in accordance with the principles of the present invention;

Fig. 2 is a cross sectional side view of the orthogonal coax to microstrip launcher of Fig. 1;

Fig. 3 is a side view of the launcher of Fig. 2 showing a quasi-channelized 50 ohm microstrip line employed therein;

Figs. 4a and 4b show top and bottom views of a circuit board comprising the microstrip line employed in the launcher of Fig. 1;

Fig. 5 is a graph showing return loss of a reduced to practice prototype of the first embodiment of the present invention;

Fig. 6 is a graph showing insertion loss of the reduced to practice prototype of the first embodiment of the present invention;

Fig. 7 is a cross sectional side view of a second embodiment of an orthogonal coax to microstrip launcher in accordance with the present invention;

Fig. 8 is a cross sectional side view of a third embodiment of an orthogonal coax to microstrip launcher in accordance with the present invention;

Fig. 9 is a graph showing return loss of a reduced to practice prototype of the third embodiment of the present invention; and

Fig. 10 is a graph showing insertion loss of the third embodiment of the of the third embodiment of the present invention.

DETAILED DESCRIPTION

[0009] Referring to the drawing figures, Fig. 1 is an exploded isometric view of a first embodiment of an orthogonal coax to microstrip launcher 10 in accordance with the principles of the present invention, and Fig. 2 is a cross sectional side view of the orthogonal coax to microstrip launcher 10 taken along its center line. This first embodiment of the launcher injects an RF signal from the bottom of the launcher 10.

[0010] The first embodiment of the orthogonal coax to microstrip launcher 10 comprises a lower metal plate 11 that has a hole 12 disposed therethrough and a plurality of threaded holes 13 therein. A coaxial connector 14 having a solid center conductor 15 is secured to the bottom of the lower metal plate 11 such that the center conductor 15 extends into the hole 12. A dielectric sleeve 21, such as a Teflon sleeve 21, for example, having a central opening 22 therethrough is disposed in the hole 12. A compressible fuzz button interconnect 20 is disposed in the central opening 22 and contacts the solid center conductor 15. A plurality of threaded holes 16 are disposed in a lateral sidewall of the lower metal plate 11.

[0011] A microstrip circuit board 30 is disposed adjacent to and abuts the lower metal plate 11. The microstrip circuit board 30 is comprised of a lower ground plane 31, a central dielectric layer 32 and an upper ground plane 33. A groove 34 is disposed in the upper ground plane 33 to expose the central dielectric layer 32, and a microstrip line or conductor 36 is formed thereon that extends from a lateral edge of the microstrip circuit board 30 to a plated via 35 that is disposed through the microstrip circuit board 30 and aligns with the fuzz button interconnect 20. A cylindrical portion of the lower ground plane 31 is also removed to provide a conductive pad 39 that contacts the via 35 and the fuzz button interconnect 20. The conductive pad 39 is insulated from the lower ground plane 31 by the gap between them formed by the removed cylindrical portion of the lower ground plane 31. The microstrip circuit board 30 has a plurality of through holes 37 that align with the plurality of threaded holes 13 in the lower metal plate 11. A plurality of plated ground vias 38 are disposed through the central dielectric layer 32 and contact the upper and lower ground planes 31, 33. A capacitive disc 25 is disposed at an internal end of the microstrip line or conductor 36, and contacts the via 35 and the end of the microstrip line or conductor 36.

[0012] An upper metal plate 40 is disposed on top of the upper ground plane 33. The upper metal plate 40 has an air channel 42 that extends from the lateral edge of the microstrip circuit board 30 to the location past the via 35. The upper metal plate 40 has a plurality of through holes 41 therethrough that align with the through holes 37 disposed through the microstrip circuit board 30 and the plurality of threaded holes 13 in the lower metal plate 11. A plurality of threaded holes 43 are disposed in a lateral sidewall of the upper metal

plate 40 that are substantially the same as the threaded holes 16 in the lower metal plate 11. A second coaxial connector 17 is secured to the threaded holes 16 in the lower metal plate 11 and the threaded holes 43 in the upper metal plate 40. A center conductor (not shown) of the second coaxial connector 17 contacts on the microstrip conductor 36.

[0013] A cover plate 44 is disposed adjacent to the upper metal plate 40 and has a plurality of through holes 45 that align with the through holes 41 in the upper metal plate 40. A plurality of threaded machine screws 46 are disposed through the through holes 45 in the cover plate 44, the through holes 41 in the upper metal plate 40, the through holes 37 disposed through the microstrip circuit board 30, and thread into the plurality of threaded holes 13 in the lower metal plate 11 to secure the orthogonal coax to microstrip launcher 10 together.

[0014] The quasi-channelized 50 ohm microstrip line 36 is connected to a capacitive disc 25 used to match the discontinuity at the orthogonal junctions shown in Fig. 2. In the center of the capacitive disc 25 is a plated via 35 that connects to a metal pad 39 on the opposite side of the circuit board 30. The metal pad 39 is isolated from the microstrip ground plane 31 by an annular clearout area (the gap) whose diameter substantially matches the outer diameter of the coaxial connector 14 within the lower metal plate 11 upon which the circuit board 20 is mounted. The metal pad 39 has a diameter designed to be substantially equal to the diameter of the fuzz button interconnect 20. The compressible fuzz button interconnect 20 is used as the coax center conductor and contacts the metal pad 39 on the microstrip circuit board 30 at one end while contacting the central conductor 15 of the coaxial connector 14 at the other end. The diameter of the capacitive disc 25 is adjusted to tune out the discontinuity at the orthogonal microstrip to fuzz button/coax junction.

[0015] Fig. 3 is a side view of the launcher 10 of Fig. 2 showing a quasi-channelized 50 ohm microstrip line or conductor 36 employed therein. Fig. 3 details the locations of the microstrip line 36 relative to the cavity 40a and the plurality of ground vias 38 that contact the upper and lower ground planes 31, 33.

[0016] Figs. 4a and 4b show top and bottom views of the microstrip circuit board 30 and its microstrip line 36 employed in the launcher of Fig. 1. The locations of all of the ground vias 38 are shown in Figs. 4a and 4b. The via 35 that contacts the fuzz button interconnect 20 is shown. The capacitive disc 25 is shown at an internal end of the microstrip line 36. The capacitive disc 25 is aligned with the conductive pad 39, the via 35, and the fuzz button interconnect 20.

[0017] Fig. 5 is a graph showing return loss of a reduced to practice prototype of the first embodiment of the orthogonal coax to microstrip launcher 10. The RF signal is shown at the input to the microstrip conductor 36 and the input to the fuzz button interconnect 20. Fig.

6 is a graph showing insertion loss of the reduced to practice prototype of the first embodiment of the orthogonal coax to microstrip launcher 10.

[0018] Fig. 7 is a cross sectional side view of a second embodiment of an orthogonal coax to microstrip launcher 10a in accordance with the present invention. The second embodiment of the orthogonal coax to microstrip launcher 10a is substantially the same as the first embodiment, but the coaxial connector 14 connects to the microstrip conductor 36 from above, through the upper metal plate 43.

[0019] The second embodiment of the orthogonal coax to microstrip launcher 10a has a solid lower metal plate 11 with a plurality of threaded holes (not shown) disposed therein. The threaded holes in the solid lower metal plate 11 are substantially the same as the threaded holes 13 described with reference to the first embodiment. A microstrip circuit board 30 is disposed adjacent to the solid lower metal plate 11. The microstrip circuit board 30 has a lower ground plane 31, a central dielectric layer 32 and an upper ground plane 33. A groove 34 is disposed in the upper ground plane 33 to expose the central dielectric layer 32, and a microstrip line or conductor 36 is formed thereon as in the first embodiment. As in the first embodiment, the microstrip circuit board 30 has a plurality of through holes (not shown) that align with the plurality of threaded holes in the lower metal plate 11. A plurality of ground vias 38 are disposed through the central dielectric layer 32 and contact the upper and lower ground planes 31, 33.

[0020] An upper metal plate 40 is disposed on top of the upper ground plane 33. The upper metal plate 40 has an air channel 42 that extends from the lateral edge of the microstrip circuit board 30 to the location past the end of the microstrip conductor 36. A through hole 45 is formed at the end of the air channel 42 that is aligned with the end of the microstrip conductor 36. The upper metal plate 40 has a plurality of through holes (not shown) therethrough that align with the through holes disposed through the microstrip circuit board 30 and the plurality of threaded holes in the lower metal plate 11. As in the first embodiment, a plurality of threaded holes (not shown) are disposed in a lateral sidewall of the upper metal plate 40 that match the threaded holes (not shown) in the lower metal plate 11. A second coaxial connector (not shown) is secured to the threaded holes in the lower and upper metal plates 11, 40. A center conductor of the second coaxial connector contacts the microstrip conductor 36. The upper metal plate 40 has a hole 45 therethrough, and a dielectric sleeve 21, such as a Teflon sleeve 21, for example, having a central opening 22 therethrough is disposed in the hole 46. A fuzz button interconnect 20 is disposed in the central opening 22 and contacts the microstrip conductor 36.

[0021] A cover plate 44 is disposed adjacent to the upper metal plate 40 and has a plurality of through holes (not shown) that align with the through holes in the upper metal plate 40. A plurality of threaded machine

screws (not shown) are disposed through the through holes in the cover plate 44, the through holes in the upper metal plate 40, the through holes disposed through the microstrip circuit board 30, and thread into the plurality of threaded holes in the lower metal plate 11 to secure the orthogonal coax to microstrip launcher 10 together. The cover plate 44 has a hole 46 therein that is aligned with the hole 45 in the upper metal plate 40. A coaxial connector 14 having a solid center conductor 15 is secured to the top of the upper metal plate 40 such that the center conductor 15 extends into the hole 46 and contacts the fuzz button interconnect 20 disposed in the dielectric sleeve 21.

[0022] As in the first embodiment, a plurality of threaded holes (not shown) are disposed in a lateral sidewall of the lower metal plate 11, and a plurality of threaded holes (not shown) are disposed in a lateral sidewall of the upper metal plate 40 that are substantially the same as the threaded holes 16 in the lower metal plate 11. A second coaxial connector (not shown) is secured to the threaded holes in the lower metal plate 11 and the threaded holes in the upper metal plate 40 as in the first embodiment. A center conductor (not shown) of the second coaxial connector contacts on the microstrip conductor 36.

[0023] In the second embodiment of the present invention, the fuzz button interconnect 20 is used to vertically launch an RF signal from the coaxial connector 14 above the circuit board 30 onto the microstrip line 36. This technique uses a direct fuzz button interconnect 20 to make contact between the microstrip line 36 and the central conductor 15 of the coaxial connector 14. An opening in the outer shield of the coaxial connector 14 is provided to prevent short-circuiting of the microstrip line 36 and to match the discontinuity at the orthogonal junction. By using the fuzz button interconnect 20, a blind solderless vertical coaxial to microstrip transition through an air cavity 40a onto the circuit board 30 is realized.

[0024] Fig. 8 is a cross sectional side view of a third embodiment of an orthogonal coax to microstrip launcher 10b in accordance with the present invention. The third embodiment of an orthogonal coax to microstrip launcher 10b is similar to the embodiment shown in Fig. 7. However, the third embodiment uses a microstrip circuit board 30 has a 90 degree radial bend therein. As is shown in Fig. 8, the central dielectric layer 32, the upper ground plane 33 and the upper ground plane 33 are radiused so that the upper ground plane 33 ends adjacent to the location of the hole in the dielectric sleeve 21.

[0025] A center pin 47 having a thin metal tab 48 at its end is disposed in the hole in the dielectric sleeve 21 and is used in cooperation with a fuzz button interconnect 20 that is disposed behind the center pin 47. The metal tab 48 at the end of the tapered portion of the coaxial center pin 47 is electrically connected to the upper ground plane 33 using an adhesive, such as an

epoxy adhesive, for example. The upper metal plate 40 is radiused to accept the radially bent microstrip circuit board 30 as is shown in Fig. 8.

[0026] The third embodiment of the orthogonal coax to microstrip launcher 10b shown in Fig. 8 is an alternative approach to vertically launch an RF signal onto a microstrip line 36 from above the circuit board 30 which involves shaping the circuit board 30 to form a 90 degree radial bend. The coaxial center pin 47 has its thin metal tab 48 adhesively secured with epoxy to the microstrip line 36 prior to installing the fuzz button interconnect 20. The performance of the vertical transition provided by the third embodiment of the orthogonal coax to microstrip launcher 10b operates to a frequency of about 18 GHz. A prototype of the third embodiment of the orthogonal coax to microstrip launcher 10b was fabricated and tested, and was found to perform properly. [0027] Thus, coaxial to microstrip orthogonal launchers that use a compressible fuzz button center conductor as a solderless interconnection have been disclosed. It is to be understood that the described embodiments are merely illustrative of some of the many specific embodiments that represent applications of the principles of the present invention. Clearly, numerous and other arrangements can be readily devised by those skilled in the art without departing from the scope of the invention.

Claims

1. An orthogonal coax to microstrip launcher (10), characterized by:
 - a lower metal plate (11) having a hole (12) disposed therethrough;
 - a coaxial connector (14) having a center conductor (15) that extends into the hole in the lower metal plate;
 - a dielectric sleeve (21) having a central opening (22) therethrough, disposed in the hole in the lower metal plate;
 - a compressible fuzz button interconnect (20) disposed in the central opening of the dielectric sleeve that contacts the center conductor of the coaxial connector;
 - a microstrip circuit board (30) disposed adjacent to the lower metal plate that comprises a lower ground plane (31), a central dielectric layer (32), an upper ground plane (33) having a microstrip conductor (36) formed thereon that extends from a lateral edge of the microstrip circuit board to a plated via (35) disposed through the microstrip circuit board that connects to a conductive pad (39) insulated from the lower ground plane that is aligned with the fuzz button interconnect, a plurality of plated ground vias (38) disposed through the central dielectric layer that contact the upper and lower
2. The orthogonal coax to microstrip launcher (10) of claim 1, characterized in that the dielectric sleeve (21) comprises a Teflon sleeve.
3. The orthogonal coax to microstrip launcher (10) of claim 1 or 2, characterized in that the capacitive disc (25) is used to match the discontinuity at the orthogonal junction between the microstrip line (36) and the coaxial connector (17).
4. The orthogonal coax to microstrip launcher (10) of any of claims 1 to 3, characterized in that the diameter of the capacitive disc (25) is adjusted to tune out the discontinuity at the junction orthogonal between the microstrip conductor (36) and the fuzz button interconnect (20) and center conductor (15) of the coaxial connector (14).
5. The orthogonal coax to microstrip launcher (10) of any of claims 1 to 4, characterized in that the fuzz button interconnect (20) comprises a blind solderless vertical coaxial to microstrip transition.
6. An orthogonal coax to microstrip launcher (10a), characterized by:
 - a lower metal plate (11);
 - a microstrip circuit board (30) disposed adjacent to the lower metal plate that comprises a lower ground plane (31), a central dielectric layer (32), an upper ground plane (33) having a microstrip conductor (36) formed thereon, and a plurality of ground vias (38) disposed through the central dielectric layer that contact the upper and lower ground planes;
 - an upper metal plate (40) disposed on the upper ground plane (33) comprising an air channel (42) that is substantially coextensive with the microstrip conductor (36), and a through hole (45) that is aligned with the inner end of the microstrip conductor, and a dielectric sleeve (21) having a central opening (22) disposed in the through hole;
 - a fuzz button interconnect (20) disposed in the central opening that contacts the microstrip

conductor;

a cover plate (44) disposed adjacent to the upper metal plate (40) having a hole (46) therein that aligns with the hole in the upper metal plate;

a coaxial connector (14) having a solid center conductor (15) that extends into the hole (46) and contacts the fuzz button interconnect disposed in the dielectric sleeve; and

a second coaxial connector (17) disposed at the end of the microstrip conductor having a center conductor that contacts the microstrip conductor.

7. An orthogonal coax to microstrip launcher (10b), characterized by:

a lower metal plate (11);

a curved microstrip circuit board (30) disposed adjacent to the lower metal plate that comprises a lower ground plane (31), a central dielectric layer (32), and an upper ground plane (33) having a microstrip conductor (36) formed thereon;

an upper metal plate (40) having an internal contour that matches the contour of the curved microstrip circuit board, and that comprises an air channel (42) that is substantially coextensive with the microstrip conductor, and having a through hole (45) that is aligned with the inner end of the microstrip conductor;

a cover plate (44) disposed adjacent to the upper metal plate having a hole (46) therein that aligns with the hole in the upper metal plate;

a dielectric sleeve (21) having a central opening (22) disposed in the hole in the upper metal plate and in the through hole of the upper metal plate;

a center pin (47) having a thin metal tab (48) at its end disposed in the hole in the dielectric sleeve (21) that is electrically connected to the upper ground plane (33) using an adhesive;

a fuzz button interconnect (20) disposed in the hole in the dielectric sleeve that contacts the center pin;

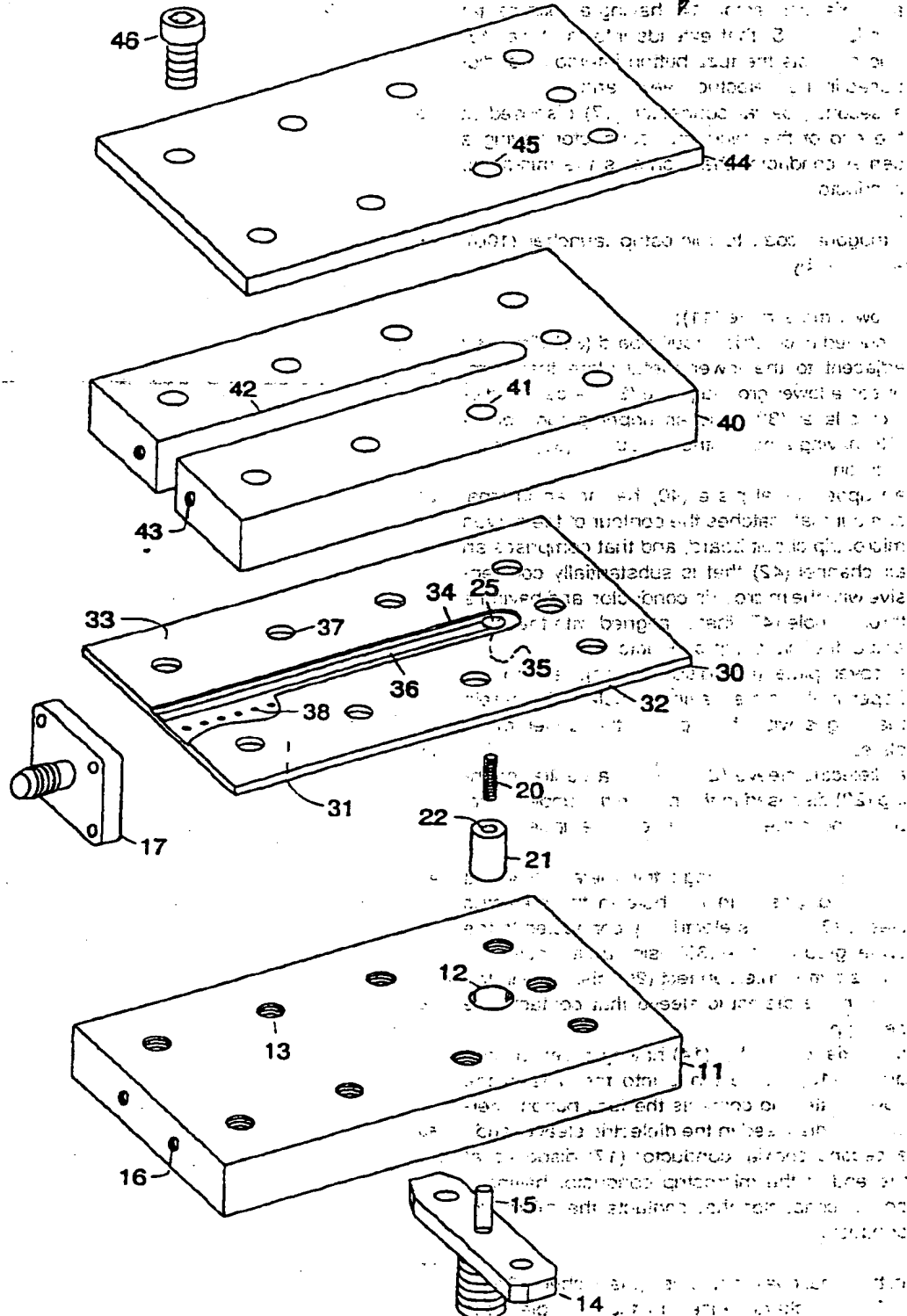
a coaxial connector (14) having a center conductor (15) that extends into the hole in the cover plate and contacts the fuzz button interconnect disposed in the dielectric sleeve; and

a second coaxial conductor (17) disposed at the end of the microstrip conductor having a center conductor that contacts the microstrip conductor.

8. The orthogonal coax to microstrip launcher (10b) of claim 6 or 7, characterized in that the dielectric sleeve (21) comprises a Teflon sleeve (21).

9. The orthogonal coax to microstrip launcher (10b) of any of claims 6 to 8, characterized in that the fuzz button interconnect (20) comprises a blind solderless vertical coaxial to microstrip transition through an air cavity (40a) onto the microstrip conductor (30).

Fig. 1



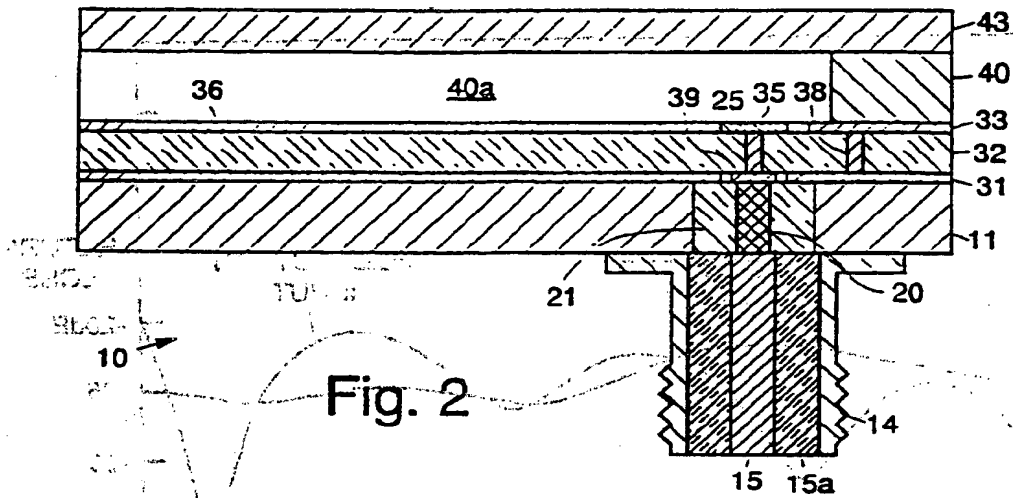
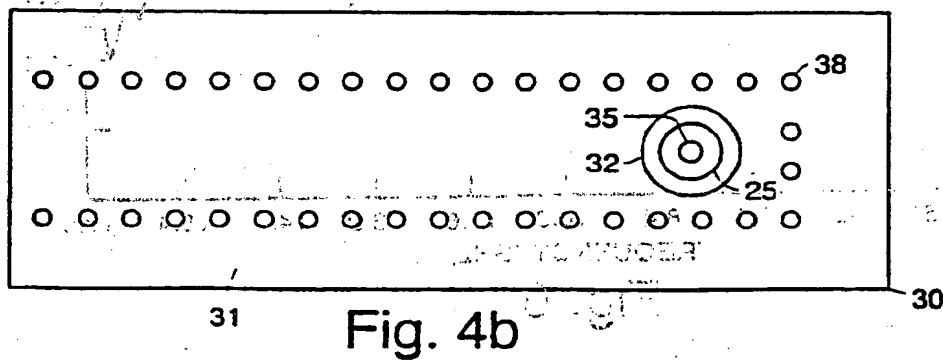
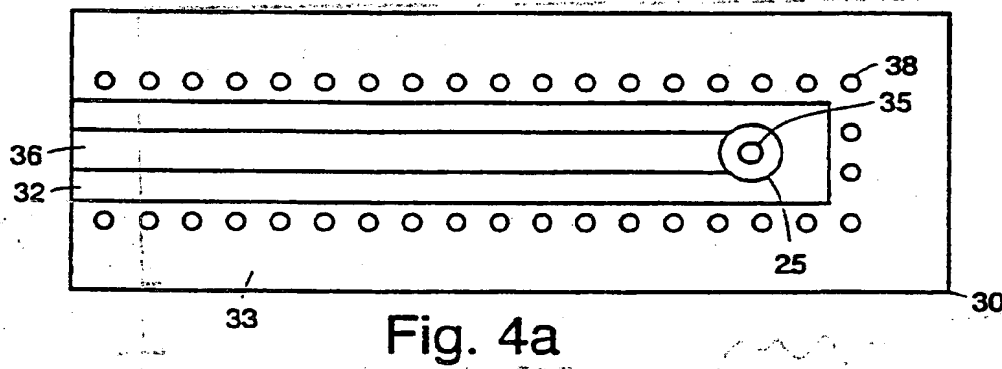
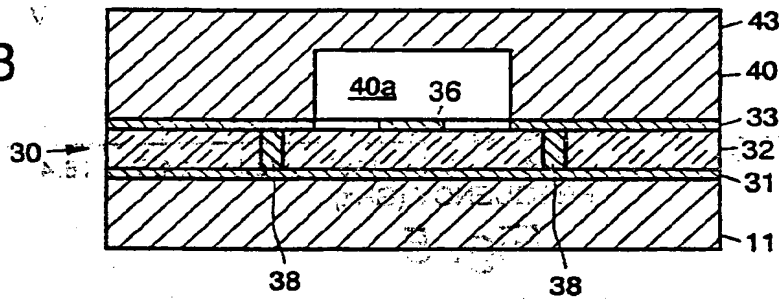


Fig. 3



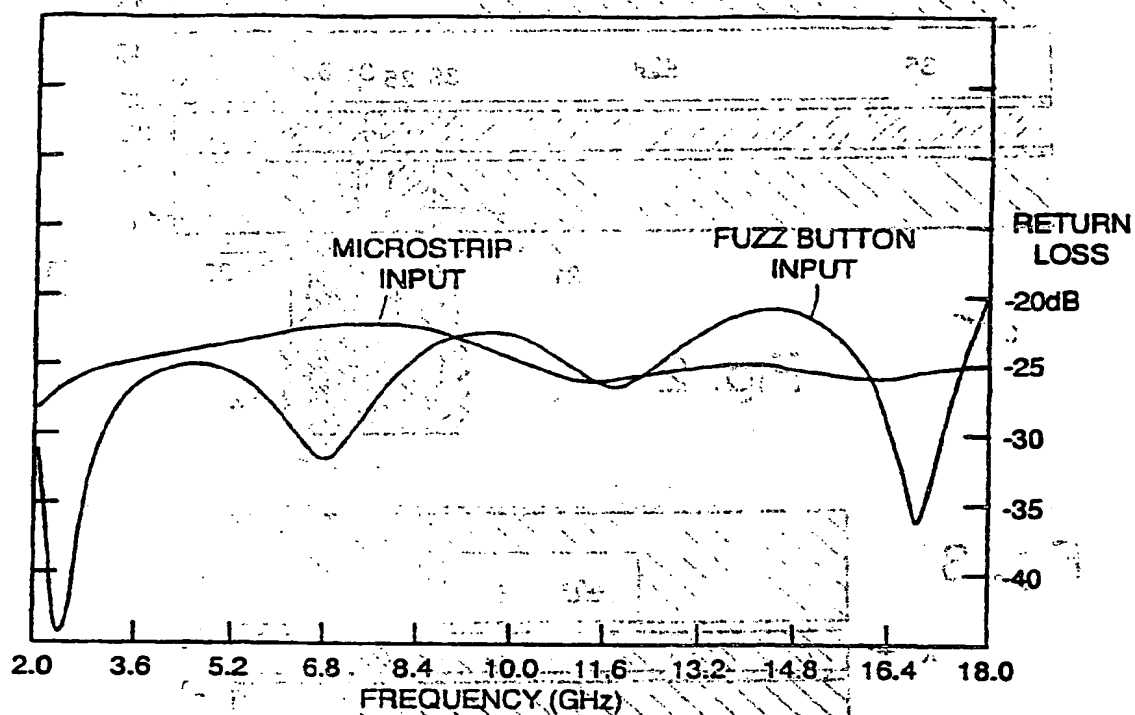


Fig. 5

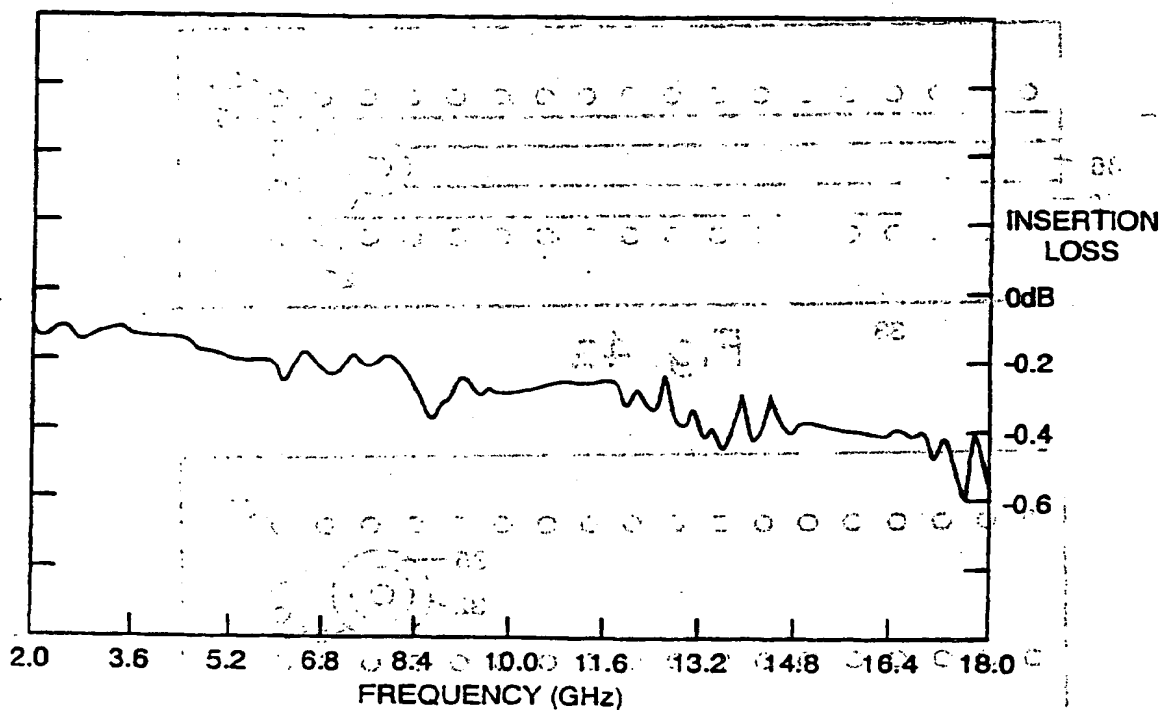
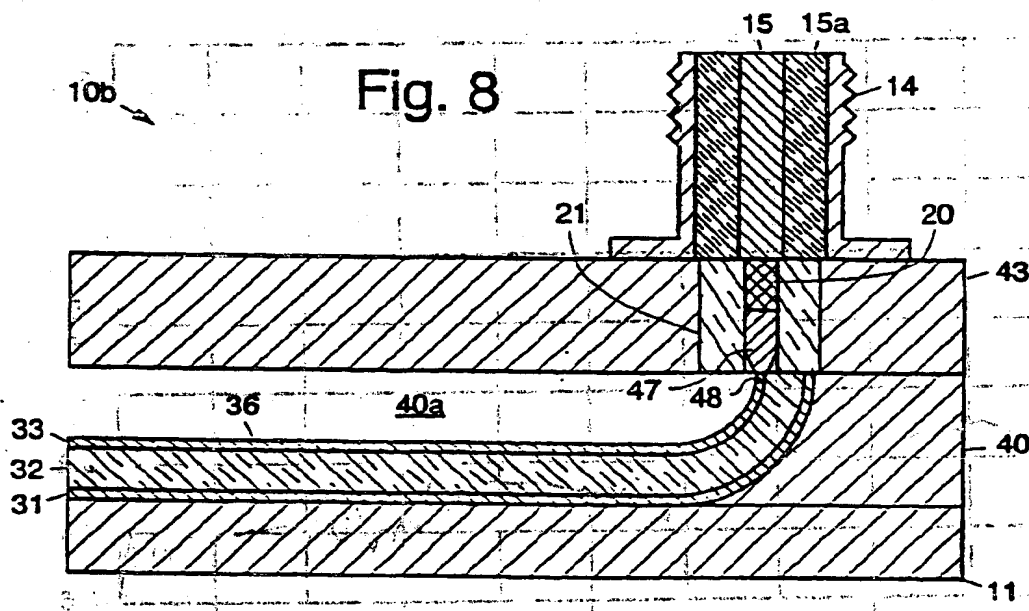
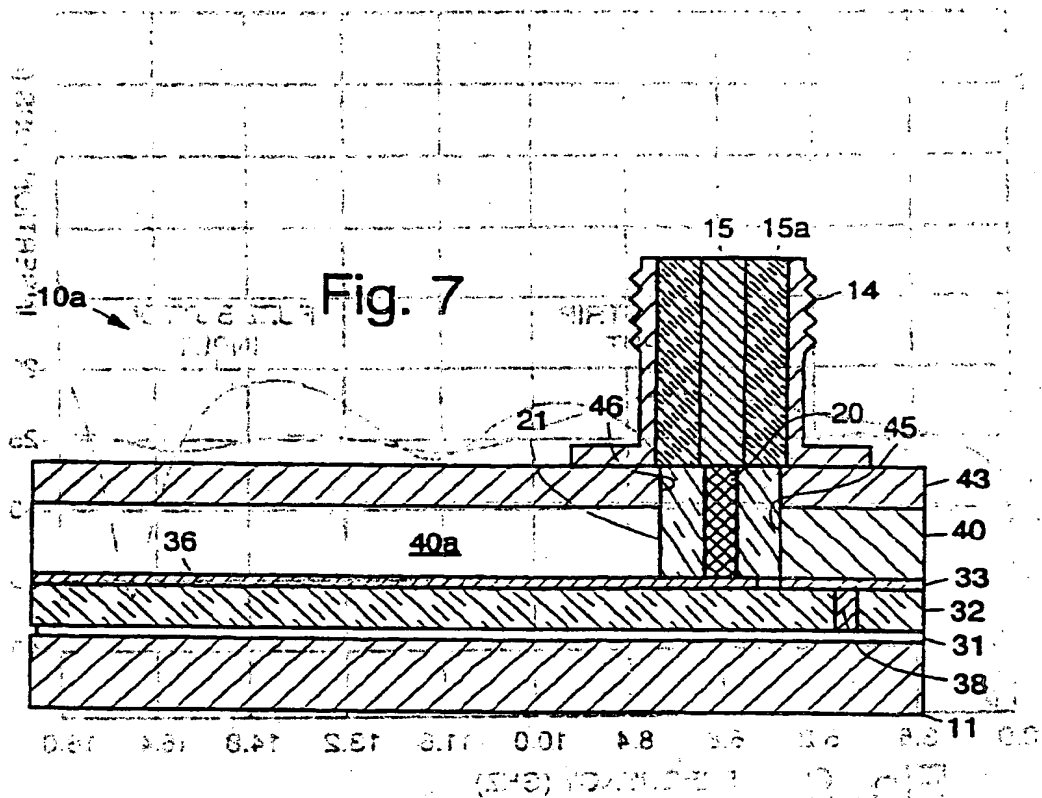
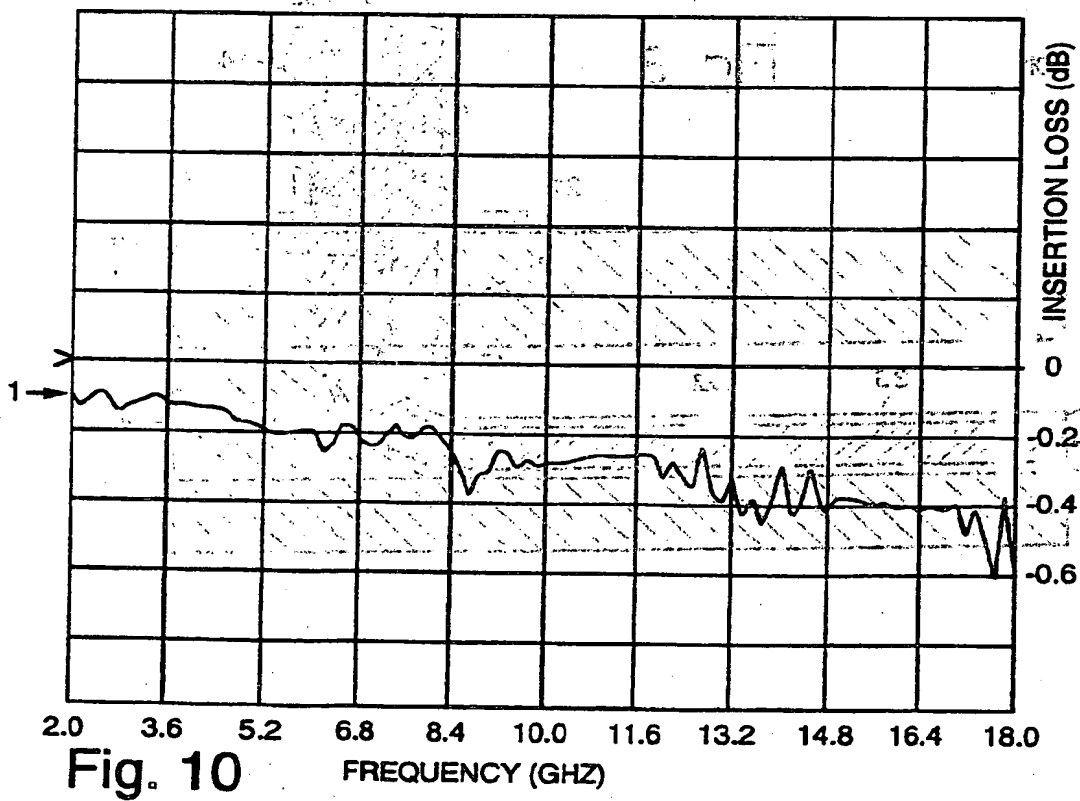
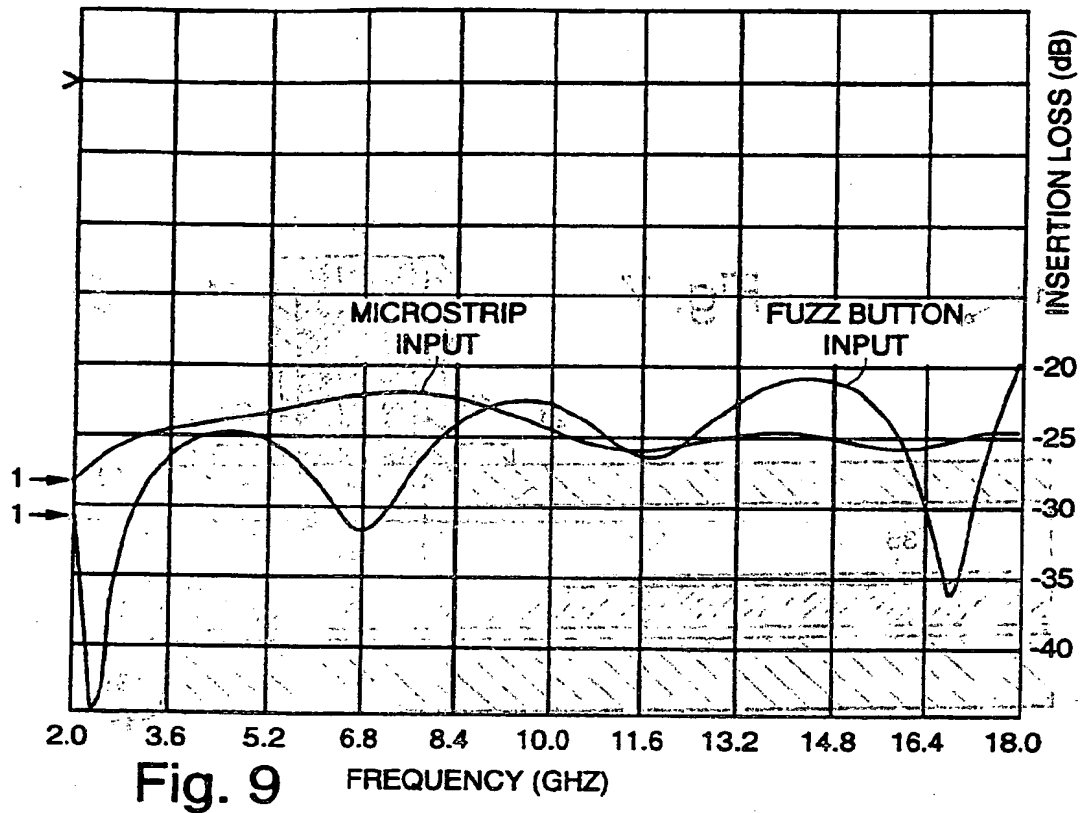


Fig. 6





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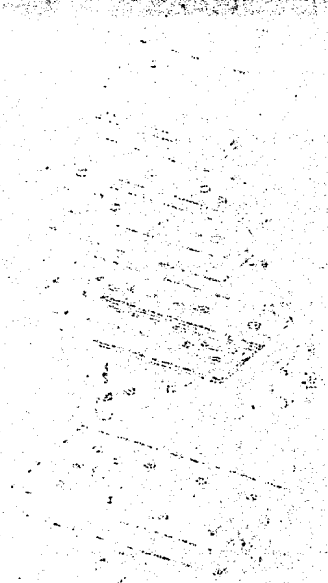
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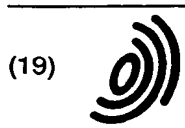
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(11) EP 0 901 181 A3

(12) EUROPEAN PATENT APPLICATION

(88) Date of publication A3:
12.04.2000 Bulletin 2000/15

(51) Int. Cl.⁷: H01P 5/08

(43) Date of publication A2:
10.03.1999 Bulletin 1999/10

(21) Application number: 98116632.5

(22) Date of filing: 02.09.1998

(84) Designated Contracting States:
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE
Designated Extension States:
AL LT LV MK RO SI

(30) Priority: 04.09.1997 US 923314

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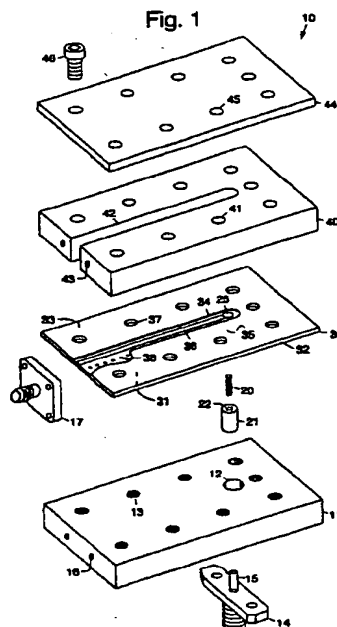
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European Patent
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EUROPEAN SEARCH REPORT

Application Number

EP 98 11 6632

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. 6)
A	US 5 618 205 A (RIDDLE ET AL.) 8 April 1997 (1997-04-08) * column 3, line 53 - column 5, line 10; figures 1-3 *	1, 5-9	H01P5/08
A	DE 43 23 928 A (HEWLETT-PACKARD CO.) 11 May 1994 (1994-05-11) * column 3, line 49 - column 4, line 64; figures 2-5 *	1, 6, 7	
A	EP 0 347 316 A (M/A-COM OMNI SPECTRA, INC.) 20 December 1989 (1989-12-20) * column 3, line 44 - column 5, line 24; figures 1-11 *	1, 6, 7	
A	EP 0 318 311 A (GENERAL ELECTRIC COMPANY) 31 May 1989 (1989-05-31) * column 7, line 9 - column 8, line 8; figure 2B *	1, 6, 7	
			TECHNICAL FIELDS SEARCHED (Int. Cl. 6)
			H01P
The present search report has been drawn up for all claims			
Place of search		Date of completion of the search	Examiner
THE HAGUE		16 February 2000	Den Otter, A
CATEGORY OF CITED DOCUMENTS			
X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same category A: technological background O: non-written disclosure P: intermediate document		T: theory or principle underlying the invention E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons &: member of the same patent family, corresponding document	

EPO FORM 1503 03/02 (P04001)

**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO. 98 11 6632**

EP 98-11 6632

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on 16-02-2000. The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

16-02-2000

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 5618205 A	08-04-1997	US 5356298 A	18-10-1994
DE 4323928 A	11-05-1994	US 5308250 A	03-05-1994
		JP 6213938 A	05-08-1994
EP 0347316 A	20-12-1989	US 4846696 A	11-07-1989
		AU 3455089 A	21-12-1989
		JP 2039601 A	08-02-1990
EP 0318311 A	31-05-1989	US 4816791 A	28-03-1989
		DE 3853135 D	30-03-1995
		DE 3853135 T	26-10-1995

